

Abstracts

A Network Reduction Technique for Microstrip Three-Dimensional Problems

C.L. Chao. "A Network Reduction Technique for Microstrip Three-Dimensional Problems." 1978 *MTT-S International Microwave Symposium Digest* 78.1 (1978 [MWSYM]): 73-75.

A method is presented for obtaining the equivalent capacitances of microstrip discontinuity structures. The method of formulation and solution is based on the resistive network reduction technique. The formulation results in a matrix eigenvalue problem and permits us to obtain more accurate results with a relatively short computer time requirement. Numerical results for a variety of discontinuity configurations are presented and compared with results published by other investigators. Excellent agreement has been obtained in all cases.

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